

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re U.S. Patent Application of**

**HAYASHI et al.**

**Application Number: 09/976,052**

**Filed: October 15, 2001**

**For: LEVEL SHIFT CIRCUIT AND SEMICONDUCTOR  
INTEGRATED CIRCUIT**

**Attorney Docket No. HITA.0110**



**Unit 2816**

**Examiner**

**Nguyen, Minh T**

#12  
Arnold  
B  
8-19-03  
afj

**Honorable Assistant Commissioner  
for Patents  
Washington, D.C. 20231**

**RESPONSE AND AMENDMENT UNDER 37 C.F.R. §1.111**

**Sir:**

This is in response to the office action dated April 9, 2003, in the above identified application, to which the period for response will expire on August 9, 2003, with a one-month extension fee filed herewith. Please amend the above identified application as follows:

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